

Dynamic resource configuration and control for an autonomous robotic vehicle

Abhishek Kulkarni, Bryce Himebaugh and Steven D Johnson ¹

SCHOOL OF INFORMATICS AND COMPUTER SCIENCE ¹,
Indiana University
Bloomington, 47401

`{adkulkar,bhimebau,sjohnson}@cs.indiana.edu`

October 23, 2009

- ▶ ERTS robotic vehicle
 - ▶ Mission
 - ▶ Architecture
- ▶ ERTS software requisites
- ▶ SYNCFS
- ▶ Cart component model
- ▶ Work in Progress
 - ▶ platform heterogeneity
 - ▶ reactive programming in limbo
 - ▶ 9p on embedded network protocols

- ▶ Lessons learned from participation in the Darpa Grand Challenge development effort
- ▶ Developed for and by the participants of introductory course on **Embedded and Real-time Systems**
- ▶ Explore embedded system design through the control of an autonomous vehicle
- ▶ Platform for local experimentation, collaborative research and instruction

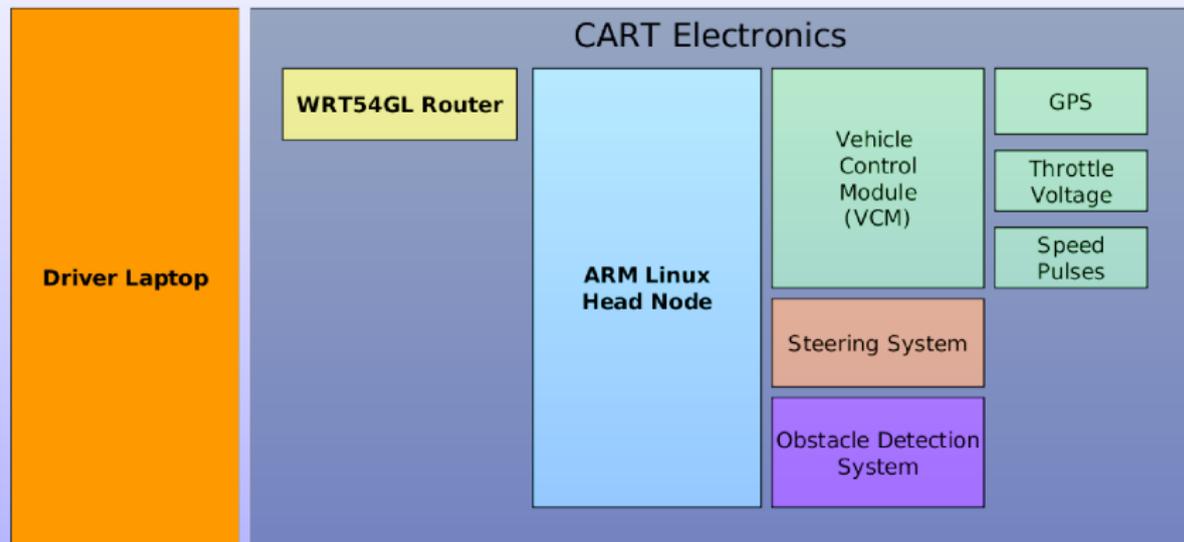


ERTS robotic vehicle

- ▶ EZGO[®] golf cart augmented with controls
- ▶ Sensors
 - ▶ GPS, compass, IMUs, vision, joystick, laser, IR, ...
- ▶ Actuators
 - ▶ VCS, steering control, obstacle avoidance, voltage throttle, ...
- ▶ Onboard LAN of ARM Linux computer nodes (CNODEs)
- ▶ Networked navigation system

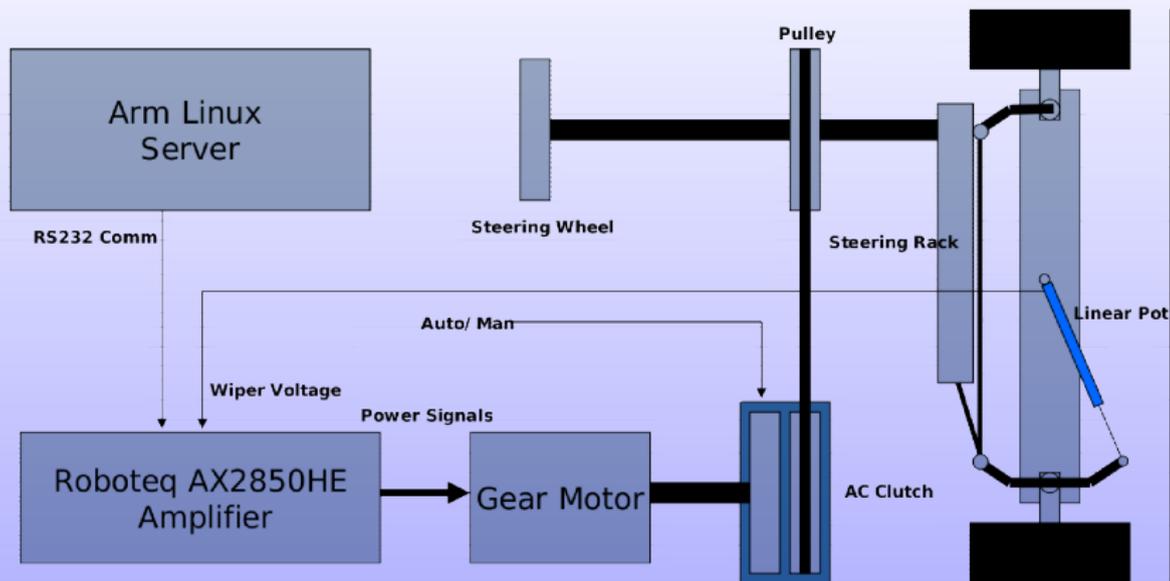


ERTS architecture



Overview of the cart's electronics

Steering system overview



Cart's steering system architecture

ERTS software requisites

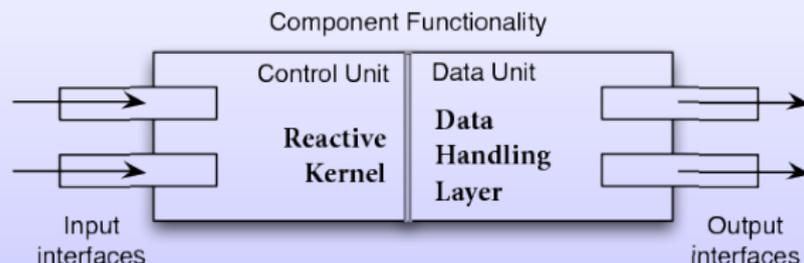
- ▶ flexible, modular and composable
- ▶ horizontal and vertical configurability
- ▶ light-weight with sufficient abstractions
- ▶ sensor network architecture
- ▶ platform heterogeneity
- ▶ implicit/explicit time synchronization

Synchronous reactive systems

Reactive systems are computer systems that continuously react to their environment at a speed determined by this environment.

- ▶ **ERTS** is essentially a reactive system
- ▶ timing behaviour can be formalized trivially
- ▶ easier to model, design and verify
- ▶ signals change only at the clock edge
- ▶ no data races and hazards

Reactive components



- ▶ control unit
 - ▶ usually implemented as a state machine
- ▶ data unit
 - ▶ processes, stores or exchanges data
- ▶ example
 - ▶ in a navigation system: GPS, IMUs, compass ...

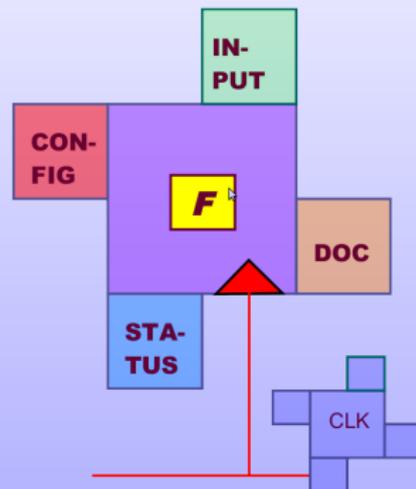
ERTS software ecosystem

- ▶ Operating System
 - ▶ Linux, RT-Linux, QNX RTOS, Plan 9, Inferno ...
- ▶ CARTFS
 - ▶ components obeying synchronous access conventions
- ▶ SYNCFS
 - ▶ synchronous file server
- ▶ Experiments

SYNCFS Overview

- ▶ synchronous file server
- ▶ modeled after a globally clocked D flip-flop
- ▶ defers writes/stats to a simulated "clock edge"
- ▶ MRSW model (multiple readers single writer)
- ▶ RAM based, w/ double buffering
- ▶ system-wide write commits

- ▶ facilitates buffer-based inter-component communication
- ▶ blocking stat as the synchronization element
- ▶ implicit CLK component
- ▶ exports elapsed ticks through the `clock` file
- ▶ 800 lines of C code, uses `npfs`



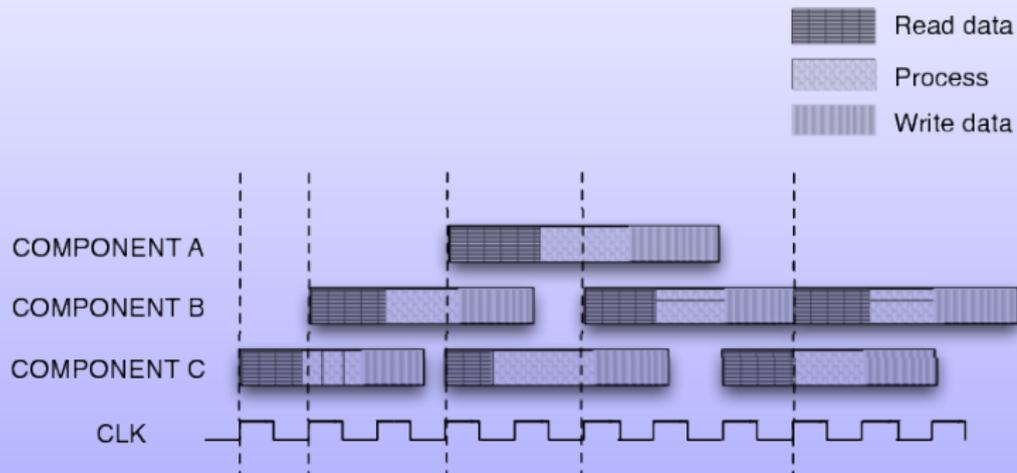
Component Framework (CARTFS)

- ▶ obeys synchronous access conventions defined by SYNCFS
- ▶ components communicate with the device or with each other
- ▶ exposes files `command`, `status`, ...
- ▶ adds itself to the global SYNCFS namespace explicitly
- ▶ components write only to their own `status` file
- ▶ uses JSON for exchanging structured data

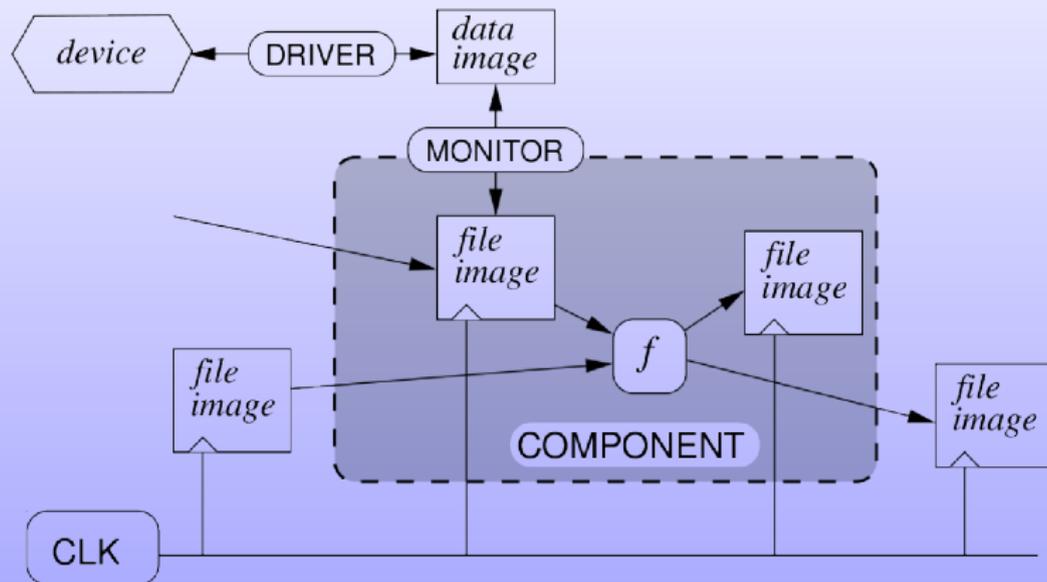
Control Loop Code

```
while True:  
    wait_for_clock()  
    read_files()  
    has_requirements?()  
    process()  
    write_files()
```

Component access flow



Component Architecture



CartFS overview

- ▶ uniform component directory structure
- ▶ *status* (*_s*) - output for the component
- ▶ *configuration* (*_c*) - contains path to the input channels
- ▶ *doc* (*_d*) - contains description of each of the status and configuration variables
 - ▶ *log* (*_log*) - contains diagnostic data
- ▶ open close operations minimized to reduce load on SYNCFS
- ▶ use seek to return to the beginning of a file

Component interface

```
1
2 $ cat /tmp/cartfs/config/config_s
3 {'clock': ['./clock', 'clock', null],
4  'percent_throttle': ['./jdriver/jdriver_s', 'percent_throttle', 0]}
5
6 $ cat /tmp/cartfs/compass/compass_s
7 {'clock': 1423, 'enable': 'True', 'heading': 124.00}
8
9 $ cat /tmp/cartfs/jdriver/jdriver_c
10 {'joystick_throttle': -0.6999999999999996, 'joystick_steering': 0.0,
11  'direction': 'Forward', 'enable': 'True', 'clock': 1538}
12
13 $
14 $ cat /tmp/cartfs/jdriver/jdriver_c_d
15 {'enable': 'True/False - stops reads on jdriver device',
16  'clock': 'The clock value on which this data was written.'}
17
18 $ cat /tmp/cartfs/jdriver/jdriver_s_d
19 {'enable': 'True/False - stops reads on jdriver device',
20  'clock': 'The clock value on which this data was written.'}
```

- ▶ 9P on Windows
 - ▶ in user space
 - ▶ allows interacting components to be written in Windows
 - ▶ facilitates collaborative research
 - ▶ makes you unhappy ☹

```
1 device = CreateFile(DOKAN_GLOBAL_DEVICE_NAME,           // lpFileName
2                     GENERIC_READ|GENERIC_WRITE,         // dwDesiredAccess
3                     FILE_SHARE_READ|FILE_SHARE_WRITE,   // dwShareMode
4                     NULL,                                // lpSecurityAttributes
5                     OPEN_EXISTING,                       // dwCreationDisposition
6                     0,                                   // dwFlagsAndAttributes
7                     NULL                                 // hTemplateFile
8                     );
```

Work in Progress

- ▶ Rewrite the component framework in Limbo
 - ▶ leverage typed channels, ADTs
 - ▶ time as a first-class notion

- ▶ Port the existing simulator to Inferno
 - ▶ graphics is a pleasure to work with

- ▶ Distributed clock synchronization
 - ▶ implement IEEE 1588 ¹

- ▶ Support 9P over embedded network protocols
 - ▶ EtherCAT, Ethernet Powerline, CAN bus ...

¹Standard for a Precise Clock Synchronization Protocol for Networked Measurement and Control Systems

Finally...

Questions?